

# **ASIC Chips and Optical Modules**





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### **Optical Control ASIC , Revolutionizing Photonics & More**

An Optical Control ASIC is a custom-designed chip that performs control and signal processing functions specifically for optical communication systems. Unlike general-purpose

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### **Charting the Path Toward 1.6T and 3.2T Optical Module**

Also, the direct 1:1 mapping between electrical and optical I/O speeds enabled by 200G/lane signaling from the application-specific integrated circuit (ASIC)

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## **CPO Is Extending The Limits Of What's Possible In AI**

The integration of these components forms the optical engine (OE). CPO refers to the integration of a GPU or ASIC together with all the components

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## **ASE Demonstrates CPO that Improves Energy Efficiency for AI**

Our Co-packaged Optics (CPO) device solves the assembly challenge of multiple optical engines with an ASIC in an integrated package, enabling 99% yield and consistent beam forming performance across ten optical phased array modules each with a 1024-element array, flip-chip assembled CMOS ASIC, and interfacing

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## **Electronic Chip Package and Co-Packaged Optics**

As we enter the post-Moore era, transistor dimensions are approaching their physical limits. Advanced packaging technologies, such as 3D chiplets

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