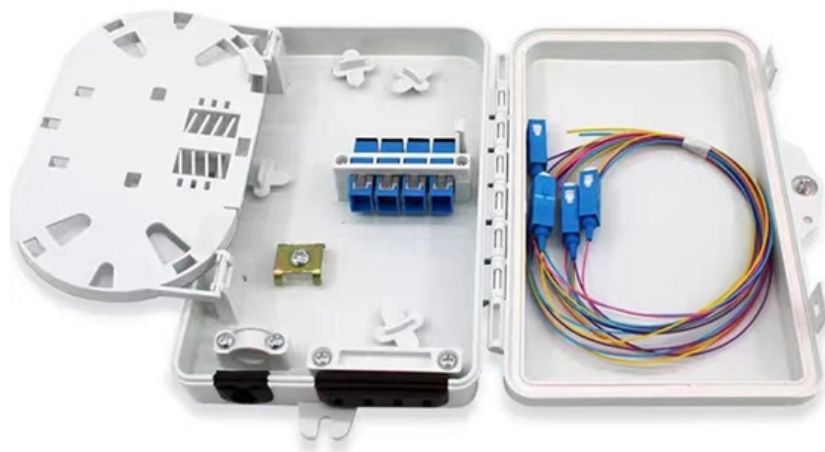


Xilinx optical modules





Xilinx optical modules

Virtex-7 10G/40G/100G Optical Networking

Powered by Xilinx Virtex-7 X485T, X690T, or V2000T FPGA, the HTG707 is a development platform that delivers the most fundamental functional blocks

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Design Approach for a FPGA based Ethernet Bridge for

PDF , On Jun 1, 2020, Murthy S and others published Design Approach for a FPGA based Ethernet Bridge for Optical Fiber Communication System , Find, read and

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ALINX AMD Xilinx Kintex UltraScale+ XCKU3P FPGA SoM system-on

The XCKU3/XCKU5 FPGA system-on-module (SoM) combines high-end AMD Xilinx Kintex UltraScale+ (TM) series device with fast DDR4 SDRAM, parallel Quad-SPI Flash and thus forms a

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XMC502

XMC502 XMC Module with Xilinx Kintex Ultrascale+ FPGA, onboard PLL and front optical option Description: XMC502 is an XMC module based on the Xilinx Kintex Ultrascale+ FPGA XCKU11P.

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Optical fiber link with Ultrascale GTH and Xillyp2p

Optical fiber link with Ultrascale GTH and Xillyp2p Introduction This guide shows how to set up a Multi-Gigabit Transceiver (MGT) on an AMD (formerly Xilinx) Ultrascale FPGA,



and how to connect it to a

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Design and FPGA Implementation of Optical Fiber Video Image

The optical fiber communication interface meets the above characteristics and has the advantages of easy-connection, so it is suitable for use in modern digital signal processing systems. In this article,

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First dual 100 Gbps Gearbox Solution to I/F CFP2 optical modules to

Incorporating a single 28nm Virtex-7 HT FPGA and Xilinx Gearbox intellectual property (IP) cores, the solution overcomes the initial hurdles of leveraging the new CFP2 optics supporting

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3U VPX FPGAs (Field Programmable Gate Arrays)

Form Factor: 3U VPX Processor: AMD (formerly Xilinx) Kintex® UltraScale(TM) XCKU115
Memory: 8 GB DDR4 Serial: 2 High-Speed Serial LVDS I/O: 44 Pairs Single-Ended I/O: 10
Optical Transmitters: 12

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Optics / FPGA Kits

On-board optical modules continue to gain acceptance in embedded, medical and mil/aero applications. Samtec offers a wide portfolio of FMC(TM),

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Pluggable Optical Interfaces and Their Compatibility with Xilinx



This white paper outlines a number of the emerging 10 to 100 Gb/s optical interfaces and how Xilinx 7 series transceivers interoperate with those interfaces.

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FPGA & VHDL KnowHow Series - 10 - Anadologic

Subject: Introduction to Serial Gigabit Transceivers - FPGA implementation and testing of Aurora 8b10b protocol over SFP modules and fiber-optic cable. Serial

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Virtex UltraScale 900 Gig Optical Networking Platform

FMC Modules Selection Guide HTG-9100: Virtex UltraScale(TM) Multi 100 Gig Optical Networking Development Platform Powered by Xilinx Virtex UltraScale 095, 125,

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Xilinx WP427 Pluggable Optical Interfaces and Their Compatibility

Introduction Pluggable optical modules are used in systems to leverage the rapid, robust long- distance data transfer of which fiber optic networks are capable. Designers must ensure that systems

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Adaptive compute acceleration co-packaged optics

Package combines Xilinx Versal and Ranovus Odin(TM) 800-Gbps CPO 2.0 cutting footprint by 75% and consumption by 40% in the optical interconnect.

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XPedite2570 , 3U VPX Kintex® UltraScale(TM) FPGA

XPedite2570 is a 3U VPX Kintex® UltraScale(TM) FPGA-based fiber-optic I/O module that optimizes cost and performance for high-bandwidth applications.



XPedite2403 , Virtex-7 FPGA-Based Fiber-Optic I/O

XPedite2403 is an AMD (formerly Xilinx) Virtex-7 FPGA-Based Conduction- or Air-Cooled Fiber-Optic I/O XMC Module.

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Virtex-7 10G/40G/100G Optical Networking

FPGA Boards Selection Guide FMC Modules Selection Guide HTG-707: Virtex-7(TM) 10G/40G/100G Optical Interface FPGA Platform Powered by Xilinx Virtex-7

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April 2018



XPedite2570 with Xilinx Kintex UltraScale FPGA Offers Cost-Optimised Performance for High-Bandwidth Applications Extreme Engineering Solutions (X-ES)

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AMD Allies with Ranovus on Data Center Photonics

The module demonstrated with AMD/Xilinx is optimized for AI workloads, but "if you look at Ethernet, it's all based on modules. That's just the

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Ranovus® Demonstrates Industry's First Adaptive

Ranovus® Demonstrates Industry's First Adaptive Compute Acceleration Co-Packaged Optics Platform with Xilinx Versal and Ranovus

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Zynq UltraScale+ MPSoC Overview

The Zynq device is a heterogeneous, multi-processing SoC built on the 16-nm FinFET technology. This Figure shows a high-level block diagram of the device architecture and key building blocks inside the

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OFC 2024: Has the CPO era arrived?

AMD and Ranovus Xilinx, working with Ranovus for many years, demonstrated a system



at OFC2023 that combines Ranovus' 800G direct-drive

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Optical fiber link with Kintex-7 GTX and Xillyp2p

This guide has presented a complete example of setting up a Multi-Gigabit Transceiver (GTX) on an AMD (formerly Xilinx) Kintex-7 FPGA and connecting it

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V1155 Quad-Port Rugged XMC FPGA Card , New Wave

V1155 Quad-Port Rugged XMC FPGA Card Bandwidth and Flexibility Designed to provide a real time high-bandwidth network interface and processing module for

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Virtex-7 FPGA Family Product Brief

The Xilinx Virtex®-7 Family of FPGAs breaks through previous physical limits to enable tomorrow's innovations. A combination of ultra high-end bandwidth and capacity boosts system performance to

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XMC502

The module has an option for the front panel Optical via SFP+ (SFP28) style connector which can operate up to 25G per lane. This allows operations such as 25Gb Ethernet or 100G across four optics.

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XPedite2570

Optical Interfaces 12-channel transmitter and receiver modules 14 Gb/s max line rate
Socketed Samtec FireFly optics I2C register interfaces

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